

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

K. TANAKA et al.

Serial No. 10/647,280

Group Art Unit: 2819

Filed: August 26, 2003

Examiner: V. Tan

For: LEVEL CONVERSION CIRCUIT AND
SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE EMPLOYING THE LEVEL
CONVERSION CIRCUIT

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 30, 2004

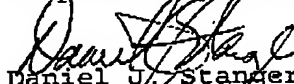
Sir:

It is respectfully requested that a One-month Extension of Time, to and including August 28, 2004, be granted in which to respond to the Office Action dated April 28, 2004 in the above-identified application.

Our Credit Card Payment Form in the amount of \$110.00 is attached in payment of the appropriate fee.

The Commissioner is hereby authorized to charge any additional payment due, or to credit any overpayment, to Deposit Account No. 50-1417.

Respectfully submitted,


Daniel J. Stanger
Registration No. 32,846
Attorney for Applicants

MATTINGLY, STANGER & MALUR
1800 Diagonal Road, Suite 370
Alexandria, Virginia 22314
(703) 684-1120
Date: August 30, 2004

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